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Solution: Lecture 1, Lecture 2, Lecture 3, Exercise 1,

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Exercise 2 2: Steady-state equivalent circuit modelling, steady-state analysis of losses and efficiency in power converters: Lecture 4: 3: Steady-state analysis of isolated dc-dc converter topologies; flyback, forward, Homework 2 Solution, Prob.3.1 m-file for M(D) plot

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